

Institute of Research In Engineering and Technology (IRET)

*International Journal of Emerging Trends in
Electrical and Electronics (IJETEE)*

(ISSN 2320-9569)

Is hereby awarding this certificate to

D.M.K Chaitanya

In recognition of the publication of the paper entitled

**HIGH SPEED & DYNAMIC SWITCHING TYPE SIGNAL GENERATION ON
FPGA FOR EMULATING TEST SIGNALS FOR NAVIGATION RECEIVER**

Published in Volume 8, Issue 1, October 2013



A handwritten signature in black ink, appearing to be "S. G. Malla".



**S. G. Malla
Director
IRET**