

*International Journal of Emerging Trends in
Electrical and Electronics (IJETEE)*

(ISSN 2320-9569)

Is hereby awarding this certificate to

R.D. Daruwala

In recognition of the publication of the paper entitled

***DESIGN AND IMPLEMENTATION OF 32 – BIT RISC PROCESSOR USING
XILINX***

Published in Volume 5, Issue 1, July 2013



A handwritten signature in black ink.

**S. G. Malla
Director
IRET**